

650V 900mohm Super-Junction Power MOSFET
AK3S65N9K0KM

Description:

This SJ device provides good FOM performance, better EMI for customer application.

Features:

- EMI-Friendly
- RoHS compliant
- Halogen-free

Applications:

- High Frequency Switching
- Quick Charging and Adapter

Key Performance Parameters:

Parameter	Value	Unit
V_{DS}	650	V
$R_{DS(on), max} @V_{GS} = 10\text{ V}$	900	mΩ
I_D	5	A



Ordering Information:

Ordering Code	Package Type	Marking Code	Form	Packing
AK3S65N9K0KM	TO-252	3S65N9K0KM	Tape Reel	See the detail package information

Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Units
V_{DS}	Drain - Source Voltage	650	V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$) ^(Note 1)	5	A
	Drain Current - Continuous ($T_C = 100^\circ\text{C}$)	3.1	A
I_{DM}	Drain Current - Pulsed ^(Note 2)	15	A
V_{GS}	Gate - Source Voltage	± 30	V
E_{AS}	Single Pulsed Avalanche Energy ^(Note 3)	24	mJ
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	50	W
dV/dT	MOSFET dv/dt ruggedness, $V_{DS} = 0 \dots 480 \text{ V}$	50	V/ns
	Reverse diode dv/dt, $V_{DS} = 0 \dots 480 \text{ V}$	50	V/ns
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction - to - Case, Steady State	2.5	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction - to - Ambient, Steady State ^(Note 4)	60	$^\circ\text{C/W}$

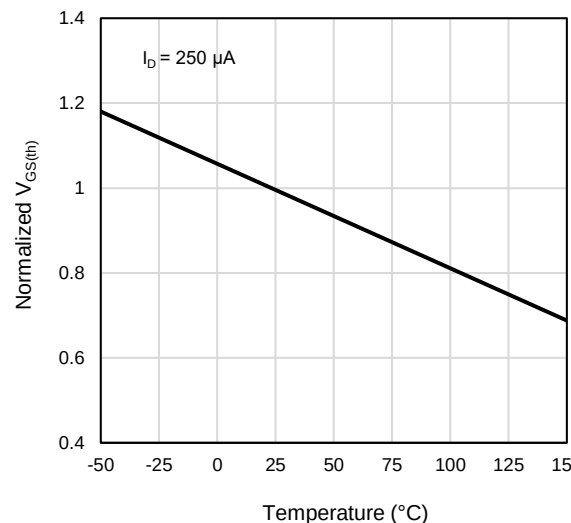
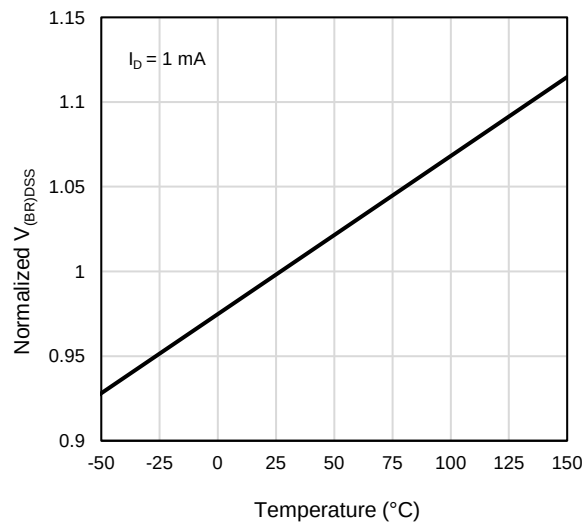
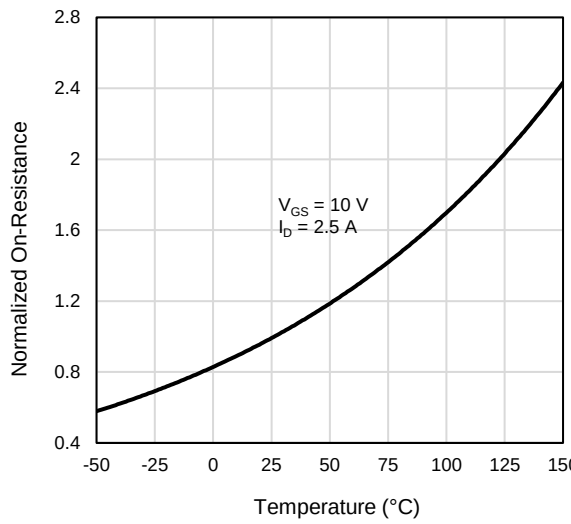
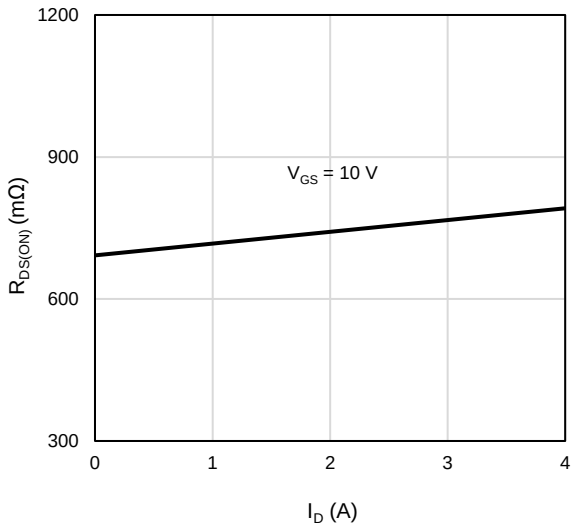
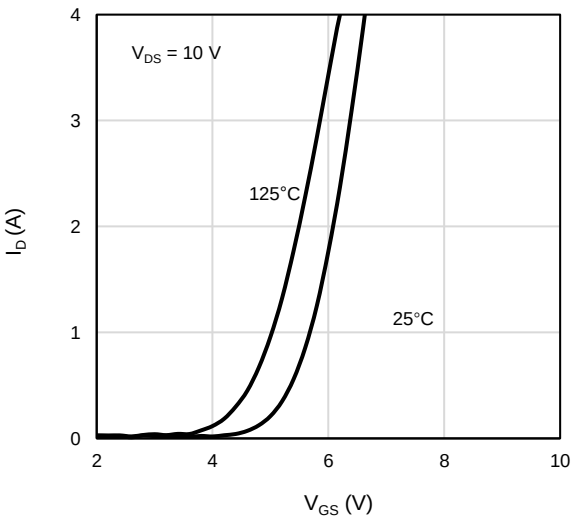
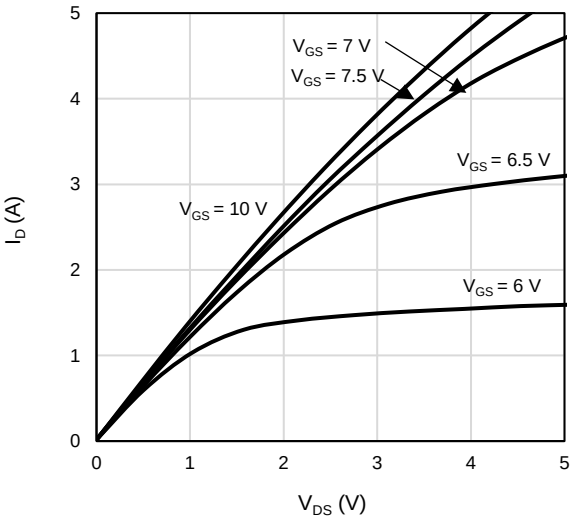
Notes:

1. The max drain current rating limited by maximum junction temperature
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. $L = 75 \text{ mH}$, $V_{DD} = 100 \text{ V}$, $I_{AS} = 0.8 \text{ A}$, $R_G = 50 \Omega$, Starting $T_J = 25^\circ\text{C}$
4. Mount on minimum PCB layout

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Static Characteristics						
V _{(BR)DSS}	Drain - Source Breakdown Voltage	V _{GS} = 0 V, I _D = 1 mA	650			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V			1	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ± 30 V, V _{DS} = 0 V			±100	nA
V _{GS(th)}	Gate Threshold voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.5	3.5	4.5	V
R _{DS(ON)}	Drain - Source on - state resistance	V _{GS} = 10 V, I _D = 2.5 A		750	900	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 400 V, V _{GS} = 0 V, f = 1 MHz		243		pF
C _{oss}	Output Capacitance			11		pF
C _{rss}	Reverse Transfer Capacitance			5.5		pF
C _{o(er)}	Effective output capacitance, energy related	V _{DS} = 0...400 V, V _{GS} = 0 V		16		pF
C _{o(tr)}	Effective output capacitance, time related	V _{DS} = 0...400 V, V _{GS} = 0 V, I _D =constant		90		pF
R _g	Gate Resistance	f = 1 MHz		27		Ω
Switching Characteristics						
t _{d(on)}	Turn On Delay Time	V _{DD} = 480 V , I _D = 2.5 A, V _{GS} = 10 V, R _G = 4 Ω		46		ns
t _r	Rise Time			17		ns
t _{d(off)}	Turn Off Delay Time			20		ns
t _f	Fall Time			10		ns
Q _g	Total Gate Charge	V _{DD} = 400 V , I _D = 2.5 A, V _{GS} = 10 V		9.5		nC
Q _{gs}	Gate - Source Charge			1.2		nC
Q _{gd}	Gate - Drain Charge			6.7		nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Body - Diode Forward Current				5	A
I _{SM}	Maximum Pulsed Body - Diode Forward Current				15	A
V _{SD}	Diode Forward Voltage	V _{GS} = 0 V, I _S = 5 A		0.96		V
t _{rr}	Reverse recovery time	V _{DD} = 480 V, I _D = 2.5 A, di/dt = 100 A/μs		91		ns
Q _{rr}	Reverse recovery charge			0.44		μC
I _{rrm}	Peak Reverse Recovery Current			9		A

Electrical Characteristics Diagrams



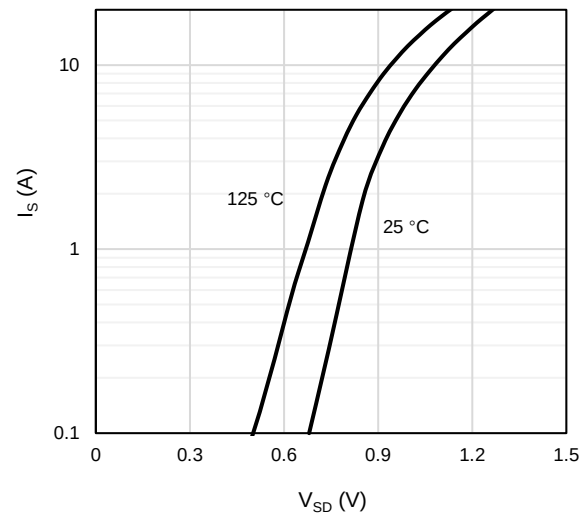


Figure 7: Body-Diode Characteristics

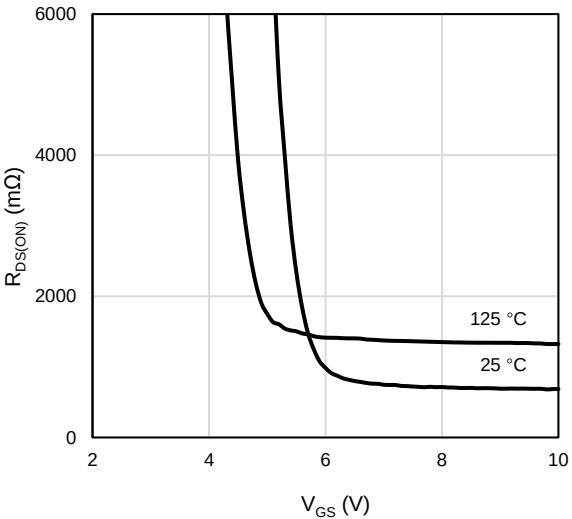


Figure 8: On-Resistance vs. Gate-Source Voltage

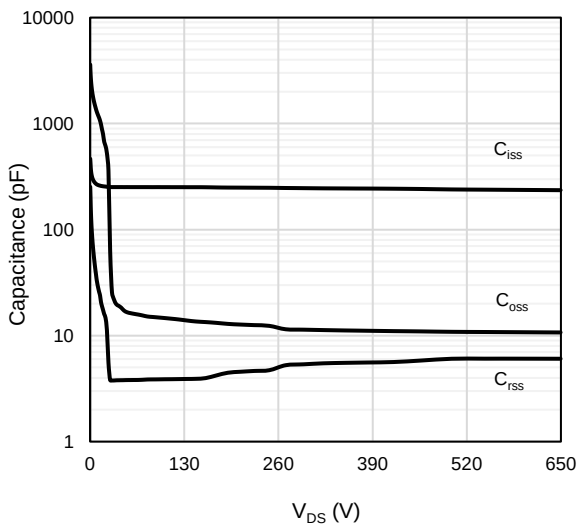


Figure 9: Capacitance Characteristics

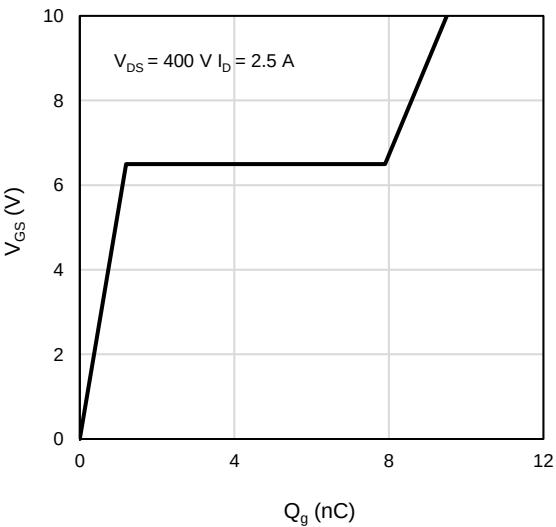


Figure 10: Gate-Charge Characteristics

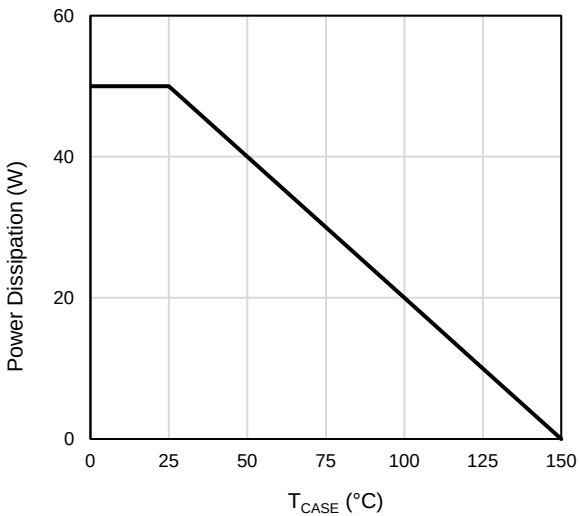


Figure 11: Power De-rating

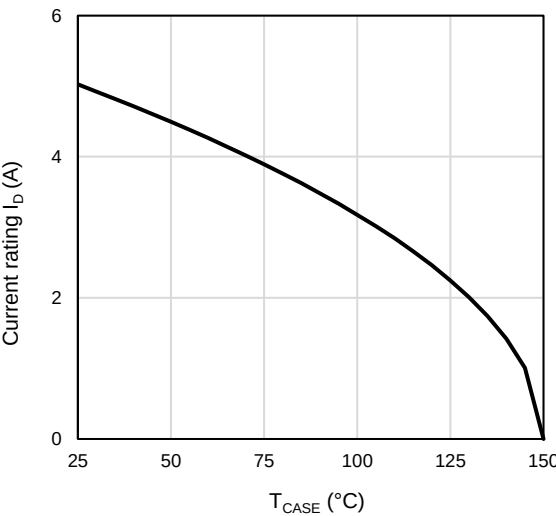


Figure 12: Current De-rating

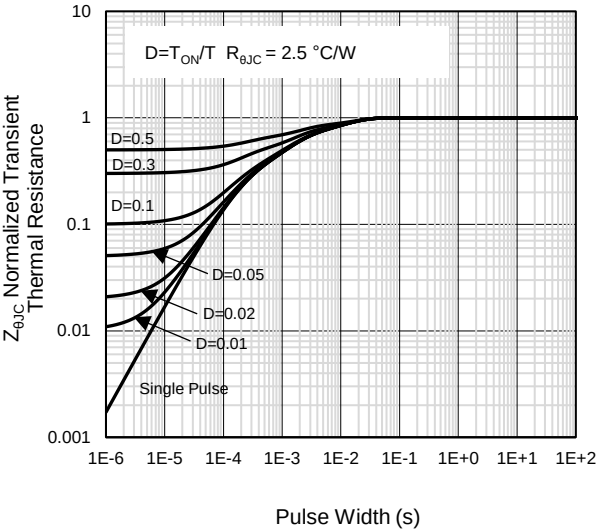


Figure 13: Normalized Maximum Transient Thermal Impedance

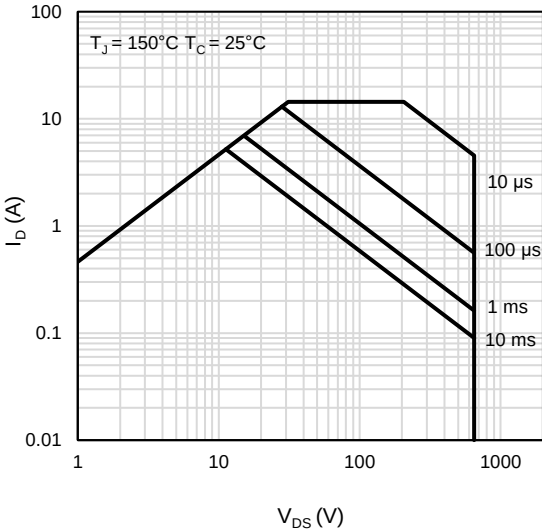
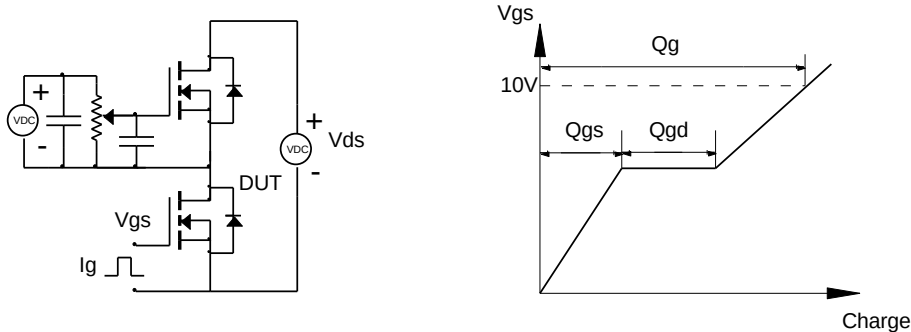


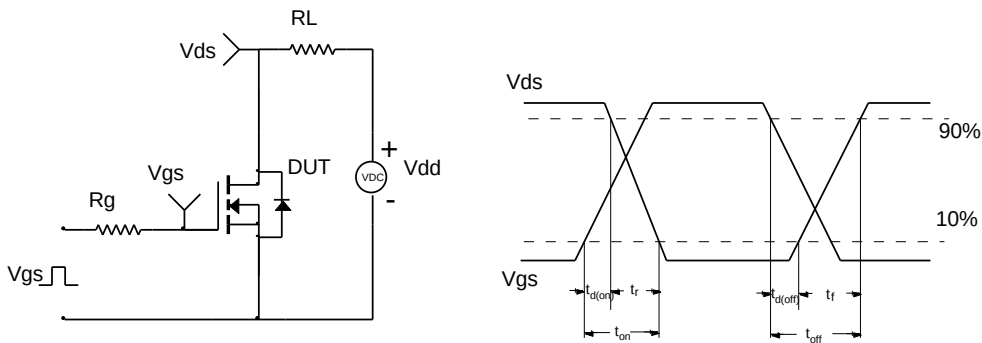
Figure 14: Maximum Forward Biased Safe Operating Area

Test Circuit and Waveform

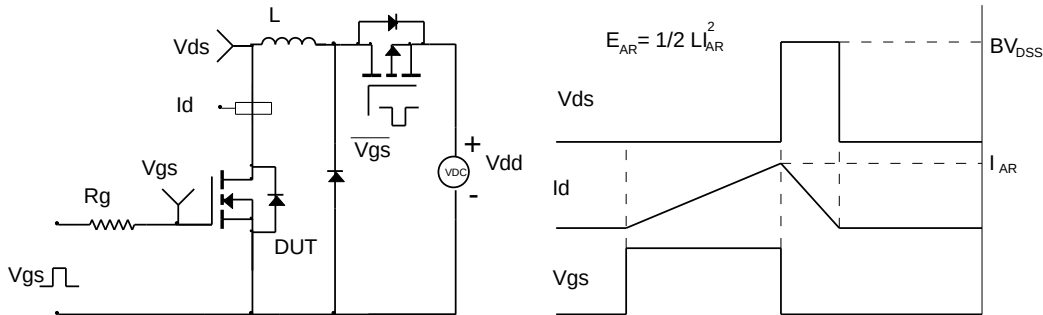
Gate Charge Test Circuit & Waveform



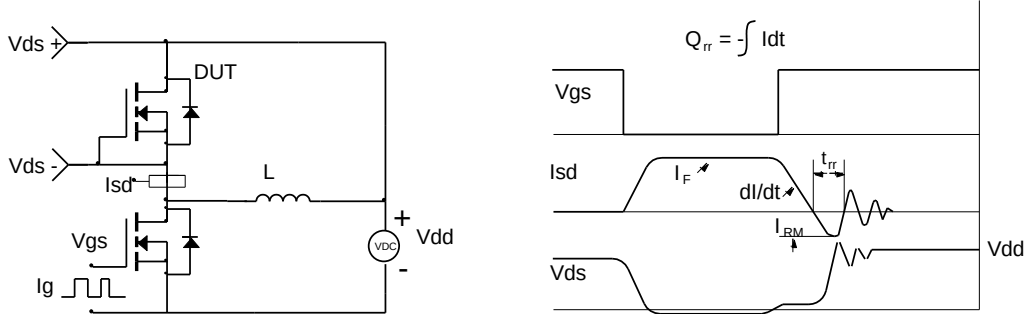
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Revision History

Revision	Released	Remark
Rev.1.0	2025	Initial Release

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.